REMARKS

Re-examination and allowance of the present application is respectfully requested.

Initially, Applicants thank the Examiner for acknowledging Applicants' claim for foreign priority, and for indicating that all of the certified copies of the priority documents have been received. Applicants further thank the Examiner for indicating that the submitted drawings are accepted.

However, Applicants respectfully traverse the Examiner's 35 U.S.C. §102(b) rejection of claims 1, 4-6, 10-12 and 14 as being anticipated by U.S. Patent 4,495,405 to TURNEY.

According to an embodiment of the present invention, a driving circuit for a display panel is disclosed that includes a plurality, such as two, transistors, a recovery capacitor and a recovery coil. Applicants submit that at least these features are not disclosed or suggested by TURNEY.

Applicants submit that a review of the structure of TURNEY discloses that it differs from the structure of the presently claimed invention. Specifically, Applicants submit that TURNEY fails to disclose or even suggest employing a plurality of transistors, a recovery capacitor and a recovery coil, as taught by the instant invention. In this regard, Applicants note that TURNEY is directed to reducing spike noise caused by the switching of a transistor, and is not directed to reducing a current with a resonance frequency flowing through an electrode of a display device, as is taught by the instant invention.

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By the current amendment, Applicants revise the claims (including claims 2, 3, 7-9 and 13, which were not rejected as being anticipated by TURNEY), to clarify that the display panel driving circuit includes the above-discussed features. Further, Applicants cancel, without prejudice, claims 5, 6, 10 and 11 in order to advance the prosecution of the present application. In view of the current amendments to the claims, Applicants submit that the ground for the 35 U.S.C. §102(b) rejection no longer exists, and respectfully request that it be withdrawn.

Applicants also respectfully traverse the judicially created obviousness-type double patenting rejection of claims 1-16 as being unpatentable over claims 1-20 of parent U.S. Patent 6,633,285. Applicants submit that the presently amended claims are not identical, and are patentably distinct from the claims of U.S. Patent 6,633,285, which is the parent of the present application. In particular, Applicants contend that the obvious-type double patenting rejection is improper because the pending claims differ from the issued claims in several significant respects. The claims of the issued patent, upon which the rejection is based, fail to recite the combination of plural transistors, a recovery capacitor, and plural frequency reducing devices, as recited in the claims of the present application. Accordingly, Applicants contend that the invention defined by the claims of the pending application is not identical to, nor would it have been obvious from, the invention defined by the claims in U.S. Patent 6,633,285. Thus, Applicants respectfully request the Examiner to withdraw the obvious-type double patenting rejection.

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SUMMARY AND CONCLUSION

In view of the fact that none of the art of record, whether considered alone or in

combination, discloses or suggests the present invention as now defined by the pending

claims, and in further view of the above amendments and remarks, reconsideration of the

Examiner's action and allowance of the present application are respectfully requested and

are believed to be appropriate.

Should the Commissioner determine that an extension of time is required in order

to render this response timely and/or complete, a formal request for an extension of time,

under 37 C.F.R. ∋1.136(a), is herewith made in an amount equal to the time period

required to render this response timely and/or complete. The Commissioner is authorized

to charge any required extension of time fee under 37 C.F.R. ∋1.17 to Deposit Account No.

19-0089.

If there should be any questions concerning this application, the Examiner is invited

to contact the undersigned at the telephone number listed below.

Respectfully Submitted, Shigeo KIGO et al.

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